

	Type	Hits	Search Text	DBs	Time Stamp
1	BRS	4045	conductive adj plug	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:21
2	BRS	0	conductive adj plug and in?situ adj (depositing deposit deposited) and silicon and phosphor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:04
3	BRS	11550	silicon and phosphor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 17:55
4	BRS	7	in?situ adj (depositing deposit deposited) and silicon and phosphor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 17:58
5	BRS	3	silicon and transient near3 phosphor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:01
6	BRS	1385	silicon and phosphor and 438/\$.cc1s.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 17:57
7	BRS	0	conductive adj plug and in?situ adj (depositing deposit deposited) and silicon and phosphorus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 17:58
8	BRS	22	in?situ adj (depositing deposit deposited) and silicon and phosphorus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:00
9	BRS	10	silicon and transient near3 phosphorus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:02
10	BRS	76	conductive adj plug and silicon same (phosphor phosphorus) same (diffuse diffusing diffusion diffused)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:05
11	BRS	16	conductive adj plug and silicon with (phosphor phosphorus) with (diffuse diffusing diffusion diffused)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:15
12	BRS	1660	438/\$.cc1s. and silicon with (phosphor phosphorus) with (diffuse diffusing diffusion diffused)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:09
13	BRS	829	438/\$.cc1s. and silicon with (phosphor phosphorus) with (diffuse diffusing diffusion diffused) with layer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:11

	Type	Hits	Search Text	DBs	Time Stamp
14	BRS	100	438/\$.ccls. and silicon with (phosphor phosphorus) with (diffuse diffusing diffusion diffused) with layer same (opening trench via)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:17
15	BRS	271	((conductive adj plug) contact) and silicon with (phosphor phosphorus) with (diffuse diffusing diffusion diffused) same (opening trench via)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:17
16	BRS	25	plug and silicon with (phosphor phosphorus) with (diffuse diffusing diffusion diffused) same (trench via opening)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:17
17	BRS	315	conductive adj plug and first near3 silicon and second near3 silicon	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:25
18	BRS	115	conductive adj plug and first near3 silicon same (trench opening via) and second near3 silicon	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:29
19	BRS	40	perkins-pamela.xa.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:33
20	BRS	44	conductive adj plug and first near3 silicon same (trench opening via) and second near3 silicon and third near3 silicon	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/25 18:37
21	BRS	0	6579788.URPN.	USPAT	2004/09/27 11:45
22	BRS	20	("5686355" "6140228" "6174795" "6245654" "6268290" "6271129" "6297147" "6326297" "6331483" "6399440" "6403465" "6403466" "6410383" "6413853" "6455419" "6465347" "6475907" "6475912" "6498095" "6524956").PN.	USPAT	2004/09/27 11:45
23	BRS	32	4818723.URPN.	USPAT	2004/09/27 11:48
24	BRS	25	5420074.URPN.	USPAT	2004/09/27 12:04
25	BRS	675	CVD near3 vacuum near3 chamber	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 12:18

	Type	Hits	Search Text	DBs	Time Stamp
26	BRS	221	CVD near3 vacuum near3 chamber and silane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 12:18
27	BRS	25	CVD near3 vacuum near3 chamber and silane and phosphine	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 12:19
28	BRS	16	CVD near3 vacuum near3 chamber and silane same phosphine	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 12:20
29	BRS	1	CVD near3 vacuum near3 chamber and silane same phosphine same alternat\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/27 12:21
30	BRS	4	("6067680" "5420074") .pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 16:32
31	BRS	2	("6067680" "5420074") .pn. and diffus\$5 and phosph\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 16:32
32	BRS	1	6479373 .pn. and diffus\$5 and phosph\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 16:33
33	BRS	3	("6067680" "5420074" "6479373") .pn. and diffus\$5 and phosph\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 16:33
34	BRS	220	438/533.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 19:02
35	BRS	631	438/565.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 19:02
36	BRS	79	438/566.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 19:02
37	BRS	288	438/657.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 19:03
38	BRS	271	438/658.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 19:03

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39	BRS	174	438/671.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 19:03
40	BRS	583	438/675.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/29 19:03

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Inventor Name Search Result

Your Search was:

Last Name = WU

First Name = WEN-CHIEH

Application#	Patent#	Status	Date Filed	Title	Inventor Name 3
<u>10766117</u>	Not Issued	030	01/27/2004	FLOATING IMPEDANCE HIGH VOLTAGE SIMULTANEOUSLY TEST CIRCUIT AND TEST METHOD THEREOF	WU, WEN-CHIEH
<u>10605007</u>	Not Issued	030	08/31/2003	METHOD FOR FABRICATING A CONDUCTIVE PLUG IN INTEGRATED CIRCUIT	WU, WEN-CHIEH
<u>09560338</u>	6199416	150	04/28/2000	MOTORCYCLE LOCK	WU, WEN-CHIEH

Inventor Search Completed: No Records to Display.

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<input type="button" value="Search"/>	

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 Time: 16:59:58


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = CHEN

First Name = YI-NAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 51
<u>10800695</u>	Not Issued	030	03/16/2004	METHOD FOR FORMING ALUMINUM-CONTAINING INTERCONNECT	CHEN, YI-NAN
<u>10783467</u>	Not Issued	030	02/20/2004	METHOD FOR FORMING CONTACT HOLES	CHEN, YI-NAN
<u>10783014</u>	Not Issued	020	02/23/2004	CONDUCTING WIRE AND CONTACT OPENING FORMING METHOD FOR REDUCING PHOTORESIST THICKNESS AND VIA RESISTANCE	CHEN, YI-NAN
<u>10768069</u>	Not Issued	030	02/02/2004	METHOD FOR FORMING BOTTLE SHAPED TRENCH	CHEN, YI-NAN
<u>10761985</u>	Not Issued	041	01/21/2004	DYNAMIC RANDOM ACCESS MEMORY CELL LAYOUT AND FABRICATION METHOD THEREOF	CHEN, YI-NAN
<u>10759058</u>	Not Issued	030	01/20/2004	METHOD FOR MANUFACTURING BIT LINE CONTACT STRUCTURE OF SEMICONDUCTOR MEMORY	CHEN, YI-NAN
<u>10751503</u>	Not Issued	030	01/06/2004	METHOD FOR FORMING SHALLOW TRENCH IN SEMICONDUCTOR DEVICE	CHEN, YI-NAN
<u>10749590</u>	Not Issued	030	01/02/2004	METHOD FOR PROCESSING PHOTORESIST	CHEN, YI-NAN
<u>10740680</u>	Not Issued	041	12/22/2003	METHOD FOR FORMING CONTACT HOLE	CHEN, YI-NAN
<u>10738472</u>	Not Issued	030	12/17/2003	MEMORY DEVICE WITH VERTICAL TRANSISTORS AND DEEP TRENCH CAPACITORS AND MANUFACTURING METHOD THEREOF	CHEN, YI-NAN
<u>10733984</u>	Not Issued	030	12/11/2003	BIT LINE CONTACT HOLE AND METHOD FOR FORMING THE	CHEN, YI-NAN

				SAME	
<u>10728983</u>	Not Issued	030	12/08/2003	STI FORMING METHOD FOR IMPROVING STI STEP UNIFORMITY	CHEN, YI-NAN
<u>10727966</u>	Not Issued	030	12/04/2003	METHOD FOR PREVENTING CONTACT DEFECTS IN INTERLAYER DIELECTRIC LAYER	CHEN, YI-NAN
<u>10727924</u>	Not Issued	030	12/04/2003	METHOD OF FORMING GEOMETRIC DEEP TRENCH CAPACITORS	CHEN, YI-NAN
<u>10727790</u>	Not Issued	030	12/04/2003	MULTI-LAYER HARD MASK STRUCTURE FOR ETCHING DEEP TRENCH IN SUBSTRATE	CHEN, YI-NAN
<u>10727730</u>	Not Issued	051	12/04/2003	METHOD OF REDUCING TRENCH ASPECT RATIO	CHEN, YI-NAN
<u>10726500</u>	Not Issued	030	12/04/2003	METHOD FOR MANUFACTURING GATE STRUCTURE FOR USE IN SEMICONDUCTOR DEVICE	CHEN, YI-NAN
<u>10726434</u>	Not Issued	030	12/03/2003	CORNER ROUNDING PROCESS FOR PARTIAL VERTICAL TRANSISTOR	CHEN, YI-NAN
<u>10724435</u>	Not Issued	030	11/28/2003	METHOD OF REDUCING THE ASPECT RATIO OF A TRENCH	CHEN, YI-NAN
<u>10722647</u>	Not Issued	030	11/26/2003	METHOD OF FORMING A MEMORY CELL WITH A SINGLE SIDED BURIED STRAP	CHEN, YI-NAN
<u>10720611</u>	Not Issued	030	11/24/2003	METHOD OF FORMING BIT LINE CONTACT	CHEN, YI-NAN
<u>10720275</u>	<u>6790765</u>	150	11/25/2003	METHOD FOR FORMING CONTACT	CHEN, YI-NAN
<u>10717582</u>	Not Issued	030	11/21/2003	CONTACT HOLE FORMING METHOD	CHEN, YI-NAN
<u>10715658</u>	Not Issued	030	11/18/2003	DAMASCENE GATE PROCESS	CHEN, YI-NAN
<u>10715616</u>	Not Issued	030	11/18/2003	FABRICATION METHOD FOR A DAMASCENE BIT LINE CONTACT PLUG	CHEN, YI-NAN
<u>10715611</u>	Not Issued	030	11/18/2003	METHOD OF FILLING BIT LINE CONTACT VIA	CHEN, YI-NAN
<u>10714952</u>	Not Issued	030	11/18/2003	SHALLOW TRENCH ISOLATION VOID DETECTING METHOD AND STRUCTURE FOR THE SAME	CHEN, YI-NAN
<u>10714001</u>	Not Issued	030	11/14/2003	METHOD OF FORMING BIT	CHEN,

				LINE CONTACT VIA	YI-NAN
<u>10709264</u>	Not Issued	030	04/26/2004	[INTERCONNECT PROCESS AND METHOD FOR REMOVING METAL SILICIDE]	CHEN, YI-NAN
<u>10709054</u>	Not Issued	030	04/09/2004	ETCHANT COMPOSITION FOR SEM IMAGE ENHANCEMENT OF P-N JUNCTION CONTRAST	CHEN, YI-NAN
<u>10708871</u>	Not Issued	093	03/30/2004	METHOD OF IMPROVING ALIGNMENT FOR SEMICONDUCTOR FABRICATION	CHEN, YI-NAN
<u>10708848</u>	Not Issued	030	03/29/2004	[INTERCONNECT STRUCTURE AND METHOD FOR FABRICATING THE SAME]	CHEN, YI-NAN
<u>10707805</u>	Not Issued	071	01/13/2004	ISOLATION STRUCTURE FOR TRENCH CAPACITORS AND FABRICATION METHOD THEREOF	CHEN, YI-NAN
<u>10707399</u>	Not Issued	041	12/10/2003	METHOD OF FORMING GATE STRUCTURE	CHEN, YI-NAN
<u>10707259</u>	Not Issued	020	12/02/2003	METHOD FOR CONTROLLING CRITICAL DIMENSION BY UTILIZING RESIST SIDEWALL PROTECTION	CHEN, YI-NAN
<u>10707081</u>	Not Issued	030	11/20/2003	[CLEANING METHOD USED IN INTERCONNECT PROCESS]	CHEN, YI-NAN
<u>10702493</u>	Not Issued	041	11/07/2003	VIA CONTACT FORMING METHOD	CHEN, YI-NAN
<u>10691173</u>	Not Issued	030	10/22/2003	MEMORY DEVICE WITH VERTICAL TRANSISTORS AND DEEP TRENCH CAPACITORS AND METHOD OF FABRICATING THE SAME	CHEN, YI-NAN
<u>10653730</u>	Not Issued	041	09/02/2003	METHOD FOR FORMING A CROWN CAPACITOR	CHEN, YI-NAN
<u>10646896</u>	Not Issued	030	08/25/2003	CONDUCTING WIRE AND CONTACT OPENING FORMING METHOD FOR REDUCING PHOTORESIST THICKNESS AND VIA RESISTANCE	CHEN, YI-NAN
<u>10645682</u>	Not Issued	071	08/21/2003	METHOD FOR FORMING UNIFORM BOTTOM ELECTRODE IN TRENCH OF TRENCH CAPACITOR	CHEN, YI-NAN
<u>10645681</u>	<u>6800535</u>	150	08/21/2003	METHOD FOR FORMING BOTTLE-SHAPED TRENCHES	CHEN, YI-NAN

<u>10640100</u>	Not Issued	030	08/13/2003	PARTIAL VERTICAL MEMORY CELL AND METHOD OF FABRICATING THE SAME	CHEN, YI-NAN
<u>10640097</u>	Not Issued	095	08/13/2003	METHOD FOR FORMING VERTICAL TRANSISTOR AND TRENCH CAPACITOR	CHEN, YI-NAN
<u>10640096</u>	Not Issued	030	08/13/2003	METHOD OF FILLING BIT LINE CONTACT VIA	CHEN, YI-NAN
<u>10639986</u>	Not Issued	041	08/13/2003	METHOD OF FABRICATING MEMORY DEVICE WITH VERTICAL TRANSISTORS AND TRENCH CAPACITORS	CHEN, YI-NAN
<u>10639419</u>	Not Issued	041	08/11/2003	METHOD FOR PREVENTING SNEAKAGE IN SHALLOW TRENCH ISOLATION AND STI STRUCTURE THEREOF	CHEN, YI-NAN
<u>10628899</u>	Not Issued	030	07/28/2003	METHOD FOR FABRICATING BOTTLE-SHAPED TRENCH CAPACITOR	CHEN, YI-NAN
<u>10206733</u>	6716696	150	07/26/2002	METHOD OF FORMING A BOTTLE-SHAPED TRENCH IN A SEMICONDUCTOR SUBSTRATE	CHEN, YI-NAN
<u>09790493</u>	<u>6426271</u>	150	02/23/2001	METHOD OF ROUNDING THE CORNER OF A SHALLOW TRENCH ISOLATION REGION	CHEN, YI-NAN
<u>09491770</u>	<u>6150207</u>	150	01/26/2000	METHOD FOR FABRICATING A CROWN CAPACITOR WITH ROUGH SURFACE	CHEN, YI-NAN

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Last Name = CHEN

First Name = YI-NAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10628895</u>	Not Issued	092	07/28/2003	METHOD FOR INCREASING CAPACITANCE OF DEEP TRENCH CAPACITORS	41 CHEN, YI-NAN
<u>10628894</u>	Not Issued	041	07/28/2003	METHOD FOR FORMING BOTTLE-SHAPED TRENCH	CHEN, YI-NAN
<u>10622965</u>	Not Issued	030	07/18/2003	DEEP TRENCH SELF-ALIGNMENT PROCESS FOR AN ACTIVE AREA OF A PARTIAL VERTICAL CELL	CHEN, YI-NAN
<u>10620869</u>	Not Issued	094	07/16/2003	SEMICONDUCTOR DEVICE HAVING TRENCH TOP ISOLATION LAYER AND METHOD FOR FORMING THE SAME	CHEN, YI-NAN
<u>10620743</u>	Not Issued	030	07/16/2003	METHOD FOR FORMING TRENCH CAPACITOR	CHEN, YI-NAN
<u>10613254</u>	<u>6780739</u>	150	07/03/2003	BIT LINE CONTACT STRUCTURE AND METHOD FOR FORMING THE SAME	CHEN, YI-NAN
<u>10612859</u>	Not Issued	093	07/03/2003	METHOD OF FORMING SHALLOW TRENCH ISOLATION WITH CHAMFERED CORNERS	CHEN, YI-NAN
<u>10611196</u>	Not Issued	030	07/01/2003	METHOD FOR ENHANCING ADHESION BETWEEN REWORKED PHOTORESIST AND UNDERLYING OXYNITRIDE FILM	CHEN, YI-NAN
<u>10605679</u>	Not Issued	071	10/17/2003	METHOD OF FABRICATING A BOTTLE SHAPED DEEP TRENCH FOR TRENCH CAPACITOR DRAM DEVICES	CHEN, YI-NAN
<u>10605678</u>	Not Issued	030	10/17/2003	METHOD OF FORMING GATE	CHEN, YI-NAN

STRUCTURE					
<u>10605401</u>	<u>6797564</u>	150	09/29/2003	[METHOD OF FORMING BIT LINES AND BIT LINE CONTACTS IN A MEMORY DEVICE]	CHEN, YI-NAN
<u>10605304</u>	Not Issued	030	09/22/2003	[SPLIT GATE FLASH MEMORY CELL AND MANUFACTURING METHOD THEREOF]	CHEN, YI-NAN
<u>10605303</u>	Not Issued	041	09/22/2003	[SPLIT GATE FLASH MEMORY CELL AND MANUFACTURING METHOD THEREOF]	CHEN, YI-NAN
<u>10605238</u>	Not Issued	020	09/17/2003	[METHOD OF REWORKING INTEGRATED CIRCUIT DEVICE]	CHEN, YI-NAN
<u>10605213</u>	Not Issued	093	09/16/2003	METHOD OF FORMING ADJACENT HOLES ON A SEMICONDUCTOR SUBSTRATE	CHEN, YI-NAN
<u>10605007</u>	Not Issued	030	08/31/2003	METHOD FOR FABRICATING A CONDUCTIVE PLUG IN INTEGRATED CIRCUIT	CHEN, YI-NAN
<u>10459327</u>	Not Issued	071	06/11/2003	METHOD FOR FORMING BIT LINE	CHEN, YI-NAN
<u>10452274</u>	Not Issued	030	06/02/2003	MANUFACTURING METHOD OF A MOSFET GATE	CHEN, YI-NAN
<u>10449297</u>	<u>6790735</u>	150	05/29/2003	METHOD OF FORMING SOURCE/DRAIN REGIONS IN SEMICONDUCTOR DEVICES	CHEN, YI-NAN
<u>10437322</u>	Not Issued	030	05/13/2003	METHOD OF FORMING METAL PLUG	CHEN, YI-NAN
<u>10413111</u>	Not Issued	030	04/14/2003	METHOD OF IMPROVING PATTERN PROFILE OF THIN PHOTORESIST LAYER	CHEN, YI-NAN
<u>10394681</u>	<u>6794270</u>	150	03/21/2003	METHOD FOR SHALLOW TRENCH ISOLATION FABRICATION AND PARTIAL OXIDE LAYER REMOVAL	CHEN, YI-NAN
<u>10379445</u>	Not Issued	093	03/03/2003	METHOD FOR FORMING BOTTLE TRENCH	CHEN, YI-NAN
<u>10375710</u>	Not Issued	030	02/27/2003	PROCESS FOR ETCHING METAL LAYER	CHEN, YI-NAN
<u>10375485</u>	<u>6790740</u>	150	02/27/2003	PROCESS FOR FILLING POLYSILICON SEAM	CHEN, YI-NAN
<u>10370441</u>	Not Issued	030	02/20/2003	METHOD OF PREVENTING	CHEN, YI-NAN

				REPEATED COLLAPSE IN A REWORKED PHOTORESIST LAYER	
<u>10329517</u>	Not Issued	061	12/27/2002	METHOD FOR FORMING BIT LINE CONTACT	CHEN, YI-NAN
<u>10322111</u>	<u>6693006</u>	150	12/17/2002	METHOD FOR INCREASING AREA OF A TRENCH CAPACITOR	CHEN, YI-NAN
<u>10268522</u>	<u>6737334</u>	150	10/09/2002	METHOD OF FABRICATING A SHALLOW TRENCH ISOLATION STRUCTURE	CHEN, YI-NAN
<u>10261648</u>	<u>6576530</u>	150	10/01/2002	METHOD OF FABRICATING SHALLOW TRENCH ISOLATION	CHEN, YI-NAN
<u>10254786</u>	Not Issued	071	09/24/2002	METHOD OF FORMING A BOTTLE-SHAPED TRENCH IN A SEMICONDUCTOR SUBSTRATE	CHEN, YI-NAN
<u>10247422</u>	Not Issued	041	09/18/2002	METHOD TO PREVENT ELECTRICAL SHORTS BETWEEN TUNGSTEN INTERCONNECTS	CHEN, YI-NAN
<u>10244988</u>	<u>6774007</u>	150	09/17/2002	METHOD OF FABRICATING SHALLOW TRENCH ISOLATION	CHEN, YI-NAN
<u>10222349</u>	<u>6709975</u>	150	08/16/2002	METHOD OF FORMING INTER-METAL DIELECTRIC	CHEN, YI-NAN
<u>10206733</u>	<u>6716696</u>	150	07/26/2002	METHOD OF FORMING A BOTTLE-SHAPED TRENCH IN A SEMICONDUCTOR SUBSTRATE	CHEN, YI-NAN
<u>10193502</u>	Not Issued	093	07/10/2002	METHOD OF METAL ETCHING POST CLEANING	CHEN, YI-NAN
<u>10163239</u>	<u>6727159</u>	150	06/04/2002	METHOD OF FORMING A SHALLOW TRENCH ISOLATION IN A SEMICONDUCTOR SUBSTRATE	CHEN, YI-NAN
<u>10162156</u>	<u>6713341</u>	150	06/03/2002	METHOD OF FORMING A BOTTLE-SHAPED TRENCH IN A SEMICONDUCTOR SUBSTRATE	CHEN, YI-NAN
<u>10159934</u>	Not Issued	041	05/29/2002	METHOD OF FORMING SHALLOW TRENCH ISOLATION	CHEN, YI-NAN
<u>09435031</u>	<u>6211006</u>	150	11/05/1999	METHOD OF FORMING A TRENCH-TYPE CAPACITOR	CHEN , YI-NAN

09009158	6055743	150	01/20/1998	METHOD AND APPARATUS FOR DRYING SEMICONDUCTOR WAFERS WITHOUT FORMING WATER MARKERS THEREON	CHEN, YI-NAN
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Last Name = WU

First Name = CHUN-YI

Application#	Patent#	Status	Date Filed	Title	Inventor Name 9
10605007	Not Issued	030	08/31/2003	METHOD FOR FABRICATING A CONDUCTIVE PLUG IN INTEGRATED CIRCUIT	WU, CHUN-YI
10216174	6738956	150	08/12/2002	CIRCUIT CONFIGURATION OF A CHIP WITH A GRAPHIC CONTROLLER INTEGRATED AND METHOD FOR TESTING THE SAME	WU, CHUN-YI
10175038	Not Issued	041	06/20/2002	METHOD FOR DETERMINING AN INTERRUPTION SOURCE	WU, CHUN-YI
09938739	Not Issued	161	08/27/2001	WHEEL RIM FOR BICYCLES	WU, CHUN-YI
09885058	6680014	150	06/21/2001	METHOD FOR MAKING CANDLE OUT OF JELLY WAX	WU, CHUN-YI
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09365179	Not Issued	161	08/02/1999	DISC BRAKE FOR BICYCLE	WU, CHUN-YI
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